

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	174	clock with (signal pulse) same loop same polarity same (link connect\$3 channel communicat\$3 bidirection\$3 direction\$2) and (@ad<"20010427" @rlad<"20010427")	US-PGPUB; USPAT	OR	ON	2006/01/20 08:04
L3	115	clock with (signal pulse) same loop same polarity same (link connect\$3 channel communicat\$3 bidirection\$3 direction\$2) and clock with (rate logic adj level speed sampl\$3) and (@ad<"20010427" @rlad<"20010427")	US-PGPUB; USPAT	OR	ON	2006/01/20 08:06
L4	70	clock with (signal pulse) same loop same polarity same (link connect\$3 channel communicat\$3 bidirection\$3 direction\$2) and clock with (rate logic adj level speed sampl\$3) and (different plurality multi\$3 (one two) adj more two mux multiplex\$3) adj3 (connect\$3 channel link line port input adj putput "i/o") and (@ad<"20010427" @rlad<"20010427")	US-PGPUB; USPAT	OR	ON	2006/01/20 08:08
L5	25	clock with (signal pulse) same loop same polarity same (back feedback revers\$3) same (link connect\$3 channel communicat\$3 bidirection\$3 direction\$2) and clock with (rate logic adj level speed sampl\$3) and (different plurality multi\$3 (one two) adj more two mux multiplex\$3) adj3 (connect\$3 channel link line port input adj putput "i/o") and (@ad<"20010427" @rlad<"20010427")	US-PGPUB; USPAT	OR	ON	2006/01/20 08:09
L6	38	clock with (signal pulse) same loop same polarity same (back opposit\$3 feedback revers\$3) same (link connect\$3 channel communicat\$3 bidirection\$3 direction\$2) and clock with (rate logic adj level speed sampl\$3) and (different plurality multi\$3 (one two) adj more two mux multiplex\$3) adj3 (connect\$3 channel link line port input adj putput "i/o") and (@ad<"20010427" @rlad<"20010427")	US-PGPUB; USPAT	OR	ON	2006/01/20 08:12

L7	25	clock with (signal pulse) same loop same polarity same (back opposit\$3 feedback revers\$3) and (two adj way bidirection\$3 bi adj direction\$2) and clock with (rate logic adj level speed sampl\$3) and (different plurality multi\$3 (one two) adj more two) adj3 (connect\$3 channel link line port input adj output "i/o") and (@ad<"20010427" @rlad<"20010427")	US-PGPUB; USPAT	OR	ON	2006/01/20 08:20
L8	1	clock with (signal pulse) same loop same polarity same (back feedback) same (opposit\$3 revers\$3) and (two adj way bidirection\$3 bi adj direction\$2) and clock with (rate logic adj level speed sampl\$3) and (different plurality multi\$3 (one two) adj more two) adj3 (connect\$3 channel link line port input adj output "i/o") and (@ad<"20010427" @rlad<"20010427")	US-PGPUB; USPAT	OR	ON	2006/01/20 08:21